



WHITE LED CHARGE PUMP CURRENT SOURCE WITH PWM BRIGHTNESS CONTROL

FEATURES

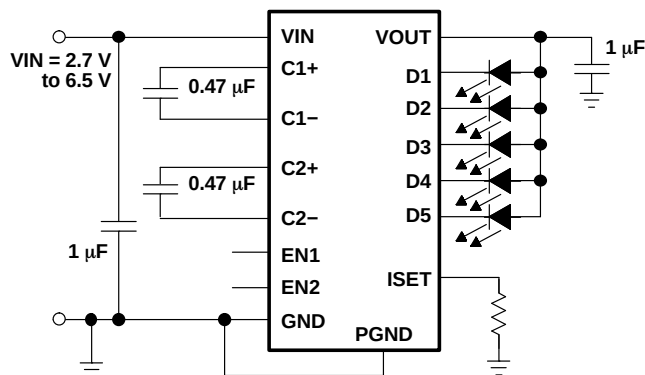
- Regulated Output Current With 0.4% Matching
- Drives up to 5 LEDs at 25 mA Each
- LED Brightness Control Through PWM Control Signal
- High Efficiency by Fractional Conversion With 1x and 1.5x Modes
- 1 MHz Switching Frequency
- 2.7 V to 6.5 V Operating Input Voltage Range
- Internal Softstart Limits Inrush Current
- Low Input Ripple and Low EMI
- Overcurrent and Overtemperature Protected
- Undervoltage Lockout With Hysteresis
- Ultra-Small 3mm x 3mm QFN Package

APPLICATIONS

- White LED Backlight for Color Displays in Cellular Phones, Smart Phones, PDAs, Handheld PCs, Digital Cameras, and Camcorders
- Key Backlight

DESCRIPTION

The TPS60230 charge pump is optimized for white LED supplies in backlit color display applications. The device provides a constant current, set by an external resistor, for each LED. The supply voltage ranges from 2.7 V to 6.5 V and is ideally suited for all applications powered by a single Li-Ion battery cell or three to four NiCd, NiMH, or Alkaline battery cells. The TPS60230 provides up to 25 mA per LED, for a total of 125 mA, for input voltages ranging from 3.1 V to 6.5 V. High efficiency is achieved by utilizing a 1x/1.5x fractional conversion technique in combination with very low dropout current sources. Additionally, the current controlled charge pump ensures low input current ripple and EMI. Only two external 1 μ F and two 0.47 μ F capacitors are required to build a complete small and low cost power supply solution. The TPS60230 switches at 1 MHz operating frequency and is available in a small 16-pin QFN (RGT) package to keep board space to a minimum.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

ORDERING INFORMATION

PACKAGED DEVICE ⁽¹⁾⁽²⁾	PACKAGE	MARKING
TPS60230RGTR	QFN	BIZ

- (1) T indicates shipment in tape and reel on a mini reel with 250 units per reel.
 (2) R indicates shipment in tape and reel with 3000 units per reel.

ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

	UNIT
V_I Supply voltage	–0.3 V to 7 V
Voltage at EN1, EN2, VOUT, ISET	–0.3 V to V_I
Output current at VOUT	200 mA
T_J Maximum junction temperature	150°C
T_A Operating free-air temperature	–40°C to 85°C
T_{st} Storage temperature	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	300°C

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATINGS⁽¹⁾

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR ABOVE $T_A = 25^\circ\text{C}$	$T_A = 70^\circ\text{C}$ POWER RATING	$T_A = 85^\circ\text{C}$ POWER RATING
16-Pin QFN (RGT)	1.9 W	20 mW/°C	1 W	760 mW

- (1) The thermal resistance junction to ambient of the QFN package is 52 °C/W.

RECOMMENDED OPERATING CONDITIONS

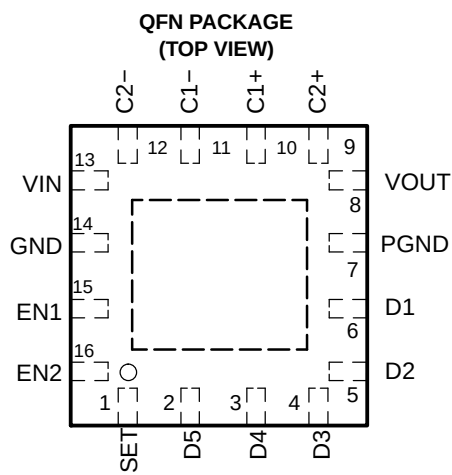
	MIN	TYP	MAX	UNIT
Supply voltage at VIN	2.7		6.5	V
Maximum output current at VOUT	125			mA
C_i Input capacitor	1			μF
C_o Output capacitor	0.47	1		μF
Flying capacitor, C1, C2	0.22	0.47		μF
Operating junction temperature	–40		125	°C

ELECTRICAL CHARACTERISTICS

$V_I = 3.6\text{ V}$, $EN1 = EN2 = V_I$, $T_A = -40^\circ\text{C}$ to 85°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
SUPPLY VOLTAGE AND CURRENT						
V _I	Input voltage range	I _O = 0 mA to 75 mA	2.7		6.5	V
I _Q	Operating quiescent current	V _I = 4.2 V, x1-mode, EN1 = EN2 = 1, ISET = 20 μA		200		μA
		I _O = 0 mA, x1.5-mode		2.1		mA
I _{SD}	Shutdown current	EN2 = EN1 = GND		0.1	1	μA
CHARGE PUMP STAGE						
V _{OUT}	Overvoltage limit	LED1 unconnected, V _I = 4.2 V		5.5		V
	Startup time	C _O = 1 μF, I _{DX} ≥ 0.9 I _{DX} , set		375		μs
	Softstart duration			160		μs
f	Switching frequency		0.75	1	1.25	MHz
η	Efficiency	V _I = 3.7 V, I _{LED} = 15 mA each, V _{DX} = 3.1 V		83%		
	Shutdown temperature	Temperature rising		160		°C
	Shutdown temperature hysteresis			20		°C
	Input current limit	EN2 = EN1 = 1, ISET = 100 μA		350		mA
CURRENT SINKS						
I _{DX}	Recommended maximum current per current sink	3.2 V ≤ V _I ≤ 6.5 V		25		mA
I _{DX}	Current into each current sink when Iset is shorted to GND	3.0 V ≤ V _I ≤ 6.5 V, ISET shorted to GND		50		mA
	Current matching between any two outputs	V _{DX} = 3.1 V, T _A = 25°C	−2%	0.4%	2%	
	Line regulation	3.2 V ≤ V _I ≤ 6.5 V, V _{DX} = 3.1 V, EN1 = EN2 = 1, ISET = 80 μA			±3%	
V _{ISET}	Reference voltage for current set	EN2 = 0, EN1 = 1		200		mV
		EN2 = 1, EN1 = 0		400		
		EN2 = 1, EN1 = 1	580	600	620	
Iset	Recommended ISET pin current range		4		130	μA
k	I _{DX} to Iset current ratio	EN2 = EN1 = 1, ISET = 80 μA	230	260	280	
V _{source}	Voltage at Dx to GND	EN2 = 0, EN1 = 1		200		mV
		EN2 = 1, EN1 = 0		300		
		EN2 = 1, EN1 = 1		400		
ENABLE 1, ENABLE 2						
V _{IH}	EN1, EN2 high level input voltage		1.3			V
V _{IL}	EN1, EN2 low level input voltage				0.3	V
	EN1, EN2 trip point hysteresis			50		mV
I _{IKG}	EN2 input leakage current	EN1, EN2 = GND or EN2 = V _I , V _I = 6.5 V	0.01		1	μA
I _I	EN1 input current	EN1 = V _I , V _I = 4.2 V		11	15	μA
V _(UVLO)	Undervoltage lockout threshold	Input voltage falling		2.1		V
	Undervoltage lockout hysteresis			50		mV
	Frequency range at PWM		0		50	kHz
	Recommended ON-time for PWM signal			2.5		μs
	Shutdown delay time	Delay time when EN1 = EN2 go to GND after which the TPS60230 shuts down completely	0.5	0.85	1.5	ms

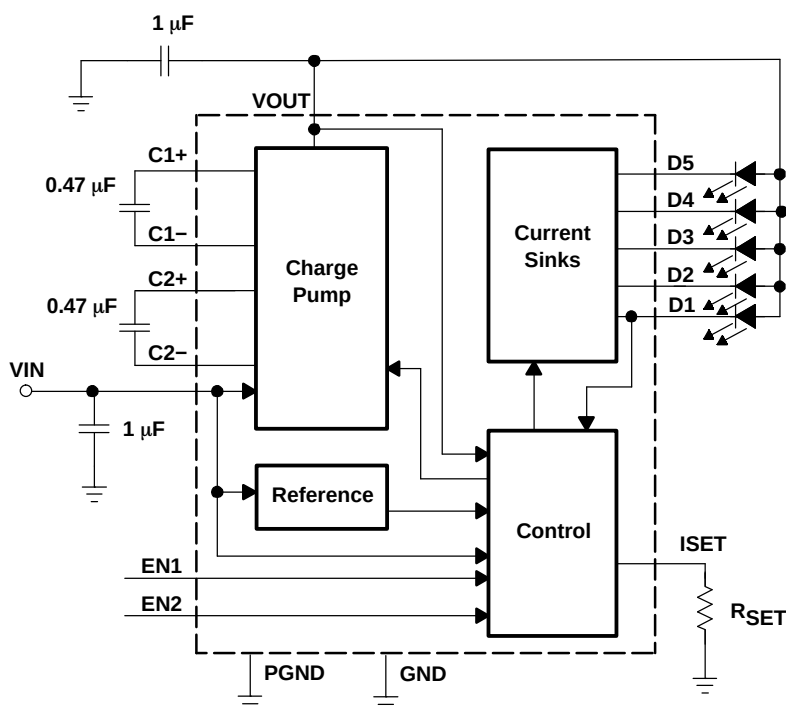
PIN ASSIGNMENT



Terminal Functions

TERMINAL NAME	NO.	I/O	DESCRIPTION
C1+	10	–	Connect to the flying capacitor C1
C1–	11	–	Connect to the flying capacitor C1
C2+	9	–	Connect to the flying capacitor C2
C2–	12	–	Connect to the flying capacitor C2
D1-D5	6-2	I	Current sink input. Connect the cathode of the white LEDs to these inputs.
EN1	15	I	Enable input. A logic high enables the converter, logic low forces the device into shutdown mode reducing the supply current to less than 1 μ A if EN2 is tied to GND.
EN2	16	I	An applied PWM signal reduces the LED current as a function of the duty cycle of the PWM signal. EN1 and EN2 can be tied together for PWM dimming between 0 mA and the maximum set with ISET. EN1 and EN2 can also be used for digital dimming with 4 steps from 0 mA to the maximum current set with ISET. See the application section for more details.
GND	14	–	Analog ground
ISET	1	I	Connect a resistor between this pin and GND to set the maximum current through the LEDs.
PGND	7	–	Power ground, connect with analog ground (GND)
VIN	13	I	Supply voltage input
VOUT	8	O	Connect the output capacitor and the anode of the LEDs to this pin.
Power PAD	–	–	Connect with PGND and GND

FUNCTIONAL BLOCK DIAGRAM



TYPICAL CHARACTERISTICS

Table of Graphs

		FIGURE
η Efficiency	vs Input voltage ($I_{LED} = 25\text{ mA}, 15\text{ mA}, 10\text{ mA per LED}$), $EN2 = 0$, $EN1 = 1$, Show hysteresis (rising and falling V_I), $V_{LED} = 3.1\text{ V}$	1
	vs Input voltage ($I_{LED} = 25\text{ mA}, 15\text{ mA}, 10\text{ mA per LED}$), $EN2 = 1$, $EN1 = 0$, Show hysteresis (rising and falling V_I), $V_{LED} = 3.1\text{ V}$	2
	vs Input voltage ($I_{LED} = 25\text{ mA}, 15\text{ mA}, 10\text{ mA per LED}$), $EN2 = EN1 = 1$, Show hysteresis (rising and falling V_I), $V_{LED} = 3.1\text{ V}$	3
I_Q No load quiescent current	vs Input voltage ($T_A = -40^\circ\text{C}, 25^\circ\text{C}, 85^\circ\text{C}$) (measured with $I_{D1} = 5\text{ mA}$)	4
Maximum output current from charge pump stage	vs Input voltage	5
f_s Switching frequency	vs Free-Air Temperature ($T_A = -40^\circ\text{C}$ to 85°C , $V_I = 3.6\text{ V}$)	6
LED current, I_{LED}	vs Duty cycle on PWM (I_{LED} max set to 20 mA) For $f = 32\text{ kHz}$ and $f = 1\text{ kHz}$, $DC = 1\%$ to 100% , $V_I = 3.6\text{ V}$	7
Line transient response	V_I and I_{D1} vs time on scope, Show LED current at D1 with $V_I = 4.2\text{ V}$ to 3.6 V to 4.2 V with $EN2 = EN1 = 11$, $5 \times 20\text{ mA}$	8
Dimming response	Show PWM signal and current at D1 vs time on scope $f = 32\text{ kHz}$, $V_I = 3.6\text{ V}$, duty cycle = 50% , $EN1 = EN2 = \text{PWM}$	9
	Show PWM signal and current at D1 vs time on scope $f = 1\text{ kHz}$, $V_I = 3.6\text{ V}$, duty cycle = 50% , $EN1 = EN2 = \text{PWM}$	10
Startup timing	$V_I = 3.6\text{ V}$, $5 \times 20\text{ mA}$, $EN1 = EN2 = 00$ changed to $EN2 = EN1 = 11$	11

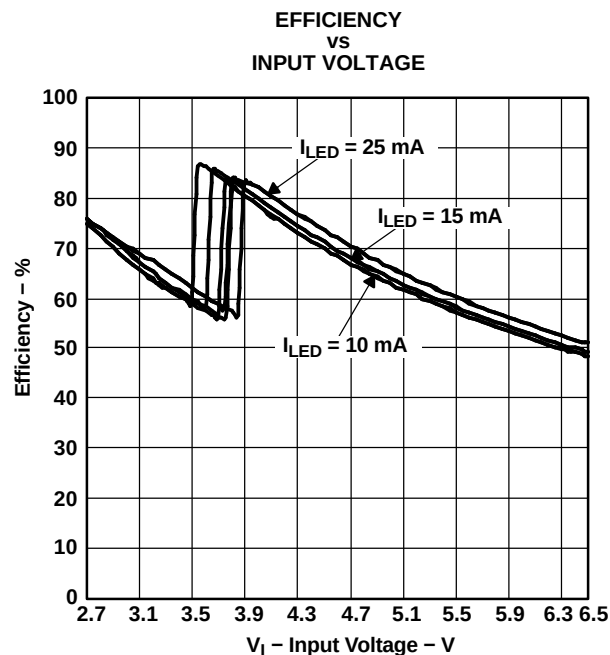


Figure 1.

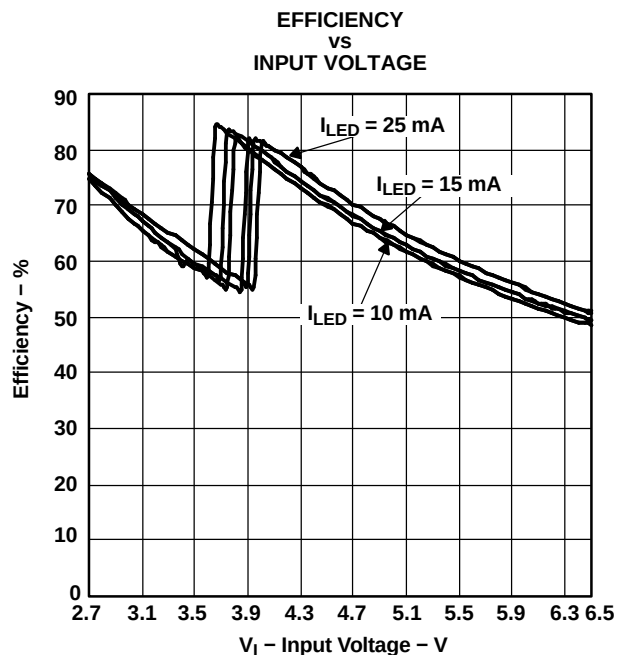


Figure 2.

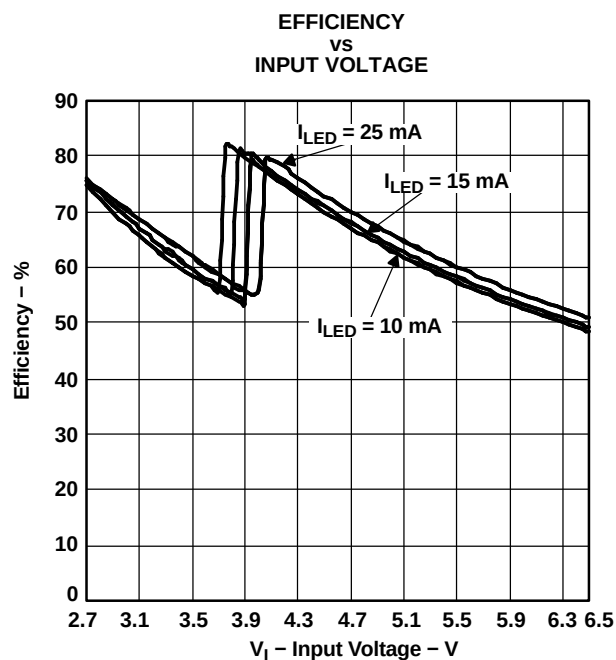


Figure 3.

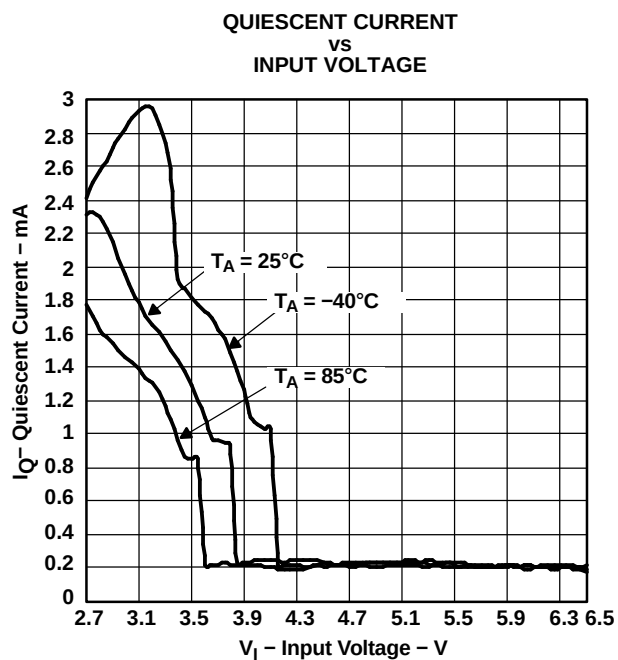


Figure 4.

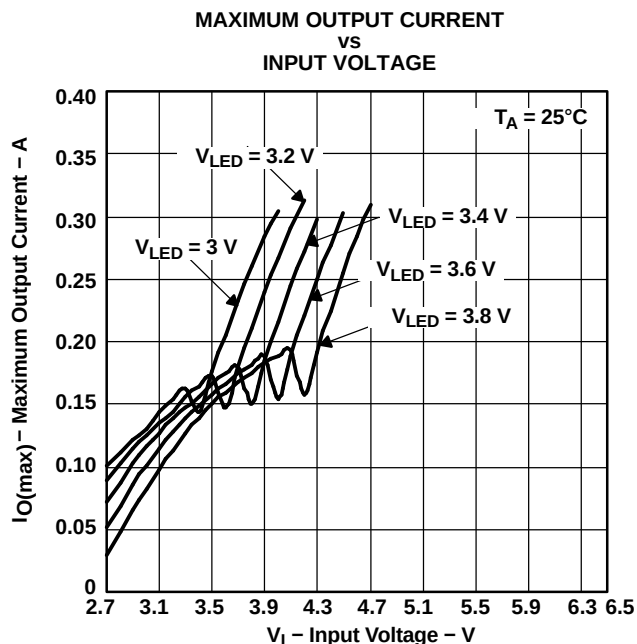


Figure 5.

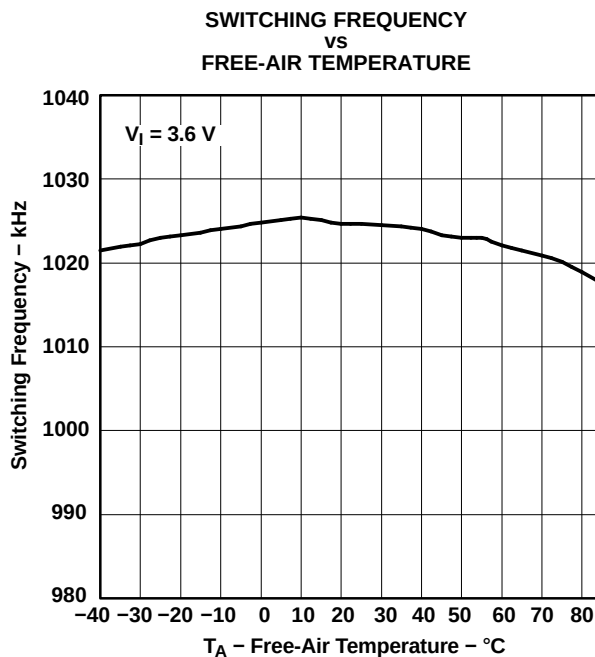


Figure 6.

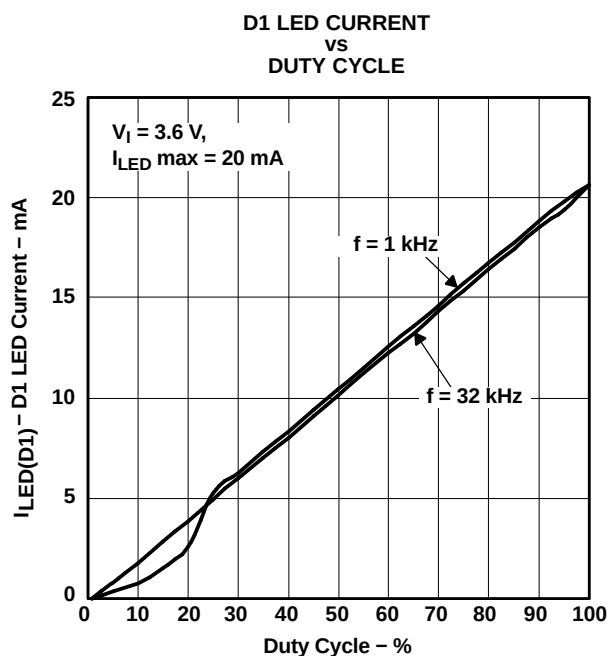


Figure 7.

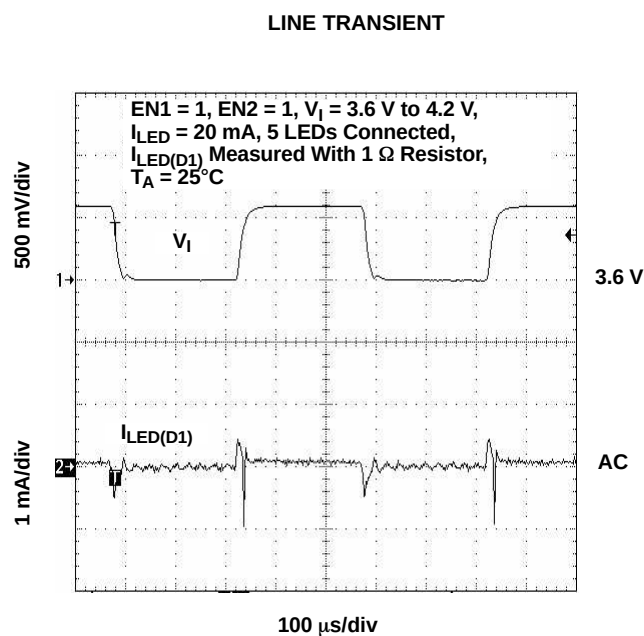


Figure 8.

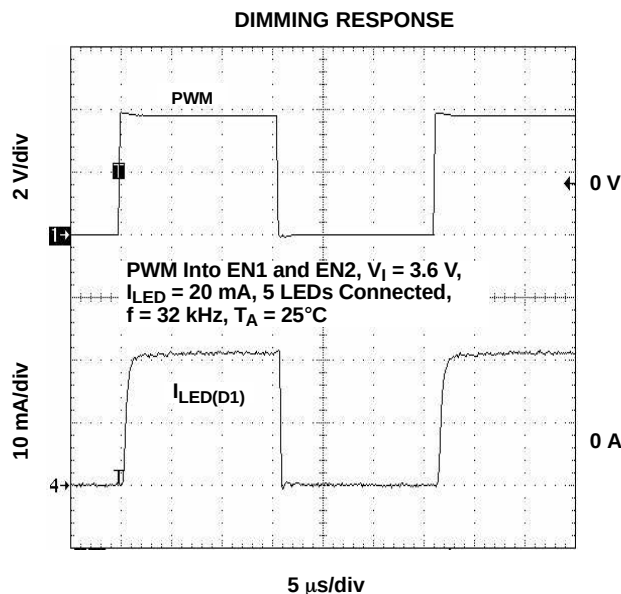


Figure 9.

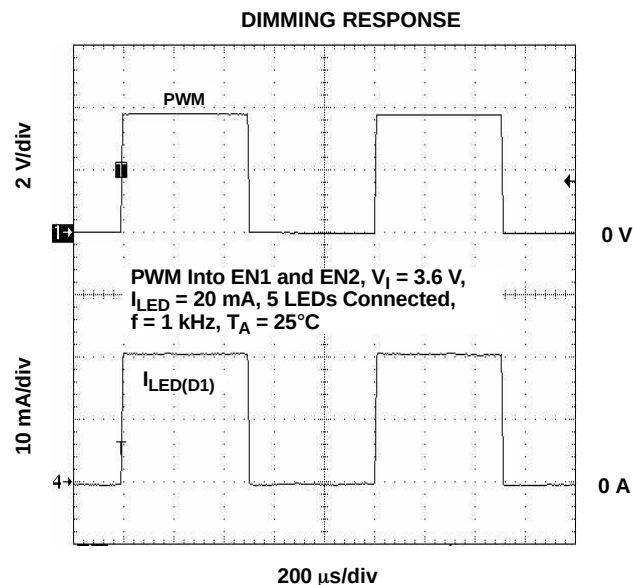


Figure 10.

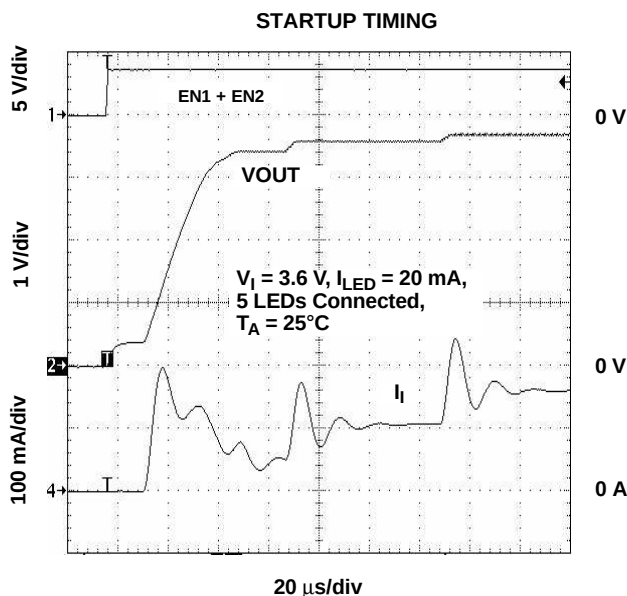


Figure 11.

DETAILED DESCRIPTION

OPERATION

The TPS60230 uses a fractional conversion charge pump to generate a supply voltage for the integrated current sinks. These current sinks are used to ensure a constant current for each LED. Depending on the input voltage and programmed LED current, the charge pump either operates in the 1x mode or in the 1.5x mode. By switching automatically between these two modes, the circuit optimizes power conversion efficiency as well as extends operating time by allowing the discharge of the battery completely.

The charge pump can generate 125 mA of output current, so each of the 5 LED outputs can be powered with up to 25 mA of current. The maximum LED current is set by a resistor connected to the ISET pin. This resistor programs a reference current, which is current mirrored to set the LED current.

DETAILED DESCRIPTION (continued)

Applying a PWM signal to the EN1 pin and/or the EN2 pin controls the LED brightness. See a detailed description in the section Analog Dimming Using ISET Pin.

LED CURRENT ADJUSTMENT (ISET)

A resistor programs a reference current, which is current mirrored to set the LED current. The voltage at the ISET pin depends on the status of EN1 and EN2. The current in each LED is typically 260 times the current through the resistor at ISET.

$$R_{ISET} = \frac{V_{ISET}}{I_{LED}} \times k$$

V_{ISET} — Voltage from ISET pin (0.2 V, 0.4 V or 0.6 V) to GND, see [Table 1](#)

I_{LED} — Current per LED from Dx pin to GND

k — I_{SET} to Dx current ratio

The LED current varies linearly from 0 mA to $I_{LED(max)}$ mA by applying a PWM signal with 0% to 100% duty cycle. The LED brightness can however also be controlled by an analog control signal that is fed into the ISET pin.

SOFT START

The TPS60230 has an internal soft start circuit to limit the inrush current during startup. This prevents possible voltage drops of the input voltage if a high impedance power source is connected to the input of the TPS60230. When the device starts up with an output voltage that is below the input voltage, the output capacitor is charged directly from the input with a current source. The output current increases linearly until the output reaches within 300 mV of the input voltage. When the programmed output current can be reached with the 1x mode, the TPS60230 terminates the soft start and begins normal operation. When the desired output current cannot be reached, the charge pump begins operation in 1.5x mode and pumps the output voltage up to the needed level to reach the programmed output current.

ENABLE (EN1, EN2)

The enable pins EN1 and EN2 are used to enable the device or set it into shutdown. The TPS60230 is enabled if one of the enable pins is pulled higher than the enable trip point of 1.3 V. The device starts up by going through the soft start routine as described in the section Soft Start. Pulling both pins to GND, after a delay, programs the device to shutdown. In shutdown, the charge pump, current sources, voltage reference, oscillator, and all other functions are turned off and the supply current is reduced to 0.1 μ A.

EN1 and EN2 can also be used for dimming. The logic levels at EN1 and EN2 set the minimum voltage at the current mirrors and the voltage at the ISET pin to GND. This sets the current at the LEDs to be either the full current or a fraction of the full current. See [Table 1](#) for further details. The maximum current through the LEDs is set by a resistor connected between ISET and GND.

EN1 and EN2 can also be used for PWM dimming. The PWM signal can either be applied to EN1 or EN2, or both inputs can be tied together and the PWM signal can be applied to both pins. Depending on the configuration, the current during PWM dimming is switched between 0 mA and its maximum (EN1 and EN2 connected to the PWM signal) or between 0 mA and 1/3 of the full LED current if EN2 = 0 and EN1 is toggled. When EN1 = 0 and EN2 is toggled, the output current can be changed between 0 mA and 2/3 of the full range.

Table 1. Enable Levels

ENABLE LEVEL		MODE	LED CURRENT
EN2	EN1		
0	0	SHUTDOWN	0
0	1	$V_{ISET} = 200 \text{ mV}$	1/3
1	0	$V_{ISET} = 400 \text{ mV}$	2/3
1	1	$V_{ISET} = 600 \text{ mV}$	Full

UNDERVOLTAGE LOCKOUT

The undervoltage lockout circuit shuts down the device when the voltage at VIN drops below a typical threshold of 2.15 V. This prevents damage to the device. The UVLO circuit allows the device to start up again after the voltage on the VIN pin has increased by about 100 mV above the UVLO lockout threshold.

SHORT CIRCUIT AND OVERTEMPERATURE PROTECTION

The current at the VOUT pin is limited typically to 250 mA. When the junction temperature exceeds 155°C, the device shuts down to protect the device from damage. After the temperature decreases to about 135°C, the device starts up again if it is enabled.

OVERVOLTAGE PROTECTION AT VOUT

The device uses the voltage at D1 to regulate voltage at VOUT. In case D1 is not connected, an overvoltage protection circuit ensures that the output voltage at VOUT does not exceed its limits. The connection of the LEDs must be started using D1 first. For all other LEDs there is no restriction in the sequence. For example, if there are only 3 LEDs used, the first LED is connected to D1 and the other two LEDs can be connected to any other of the D2 to D5 pins.

THEORY OF OPERATION/DESIGN PROCEDURE

Capacitor Selection

Ceramic capacitors such as X5R or X7R are recommended to be used with the TPS60230. For the two flying capacitors C1 and C2, it is important to use low ESR capacitors to avoid unnecessary efficiency losses. Low ESR capacitors on VOUT reduce the ripple voltage on the supply of the current sources. [Table 2](#) lists capacitor types that have been tested with the TPS60230.

Table 2. Capacitors

PART	VALUE	VOLTAGE	MANUFACTURER	SIZE	WEBSITE
C1608X5R1A105M	1 μ F	10 V	TDK	0603	www.component.tdk.com
C1608X5R1A474M	0.47 μ F	10 V		0603	
C2012X7R1C105M	1 μ F	16 V		0805	
LMK107BJ105MA	1 μ F	10 V	Taiyo Yuden	0603	www.t-yuden.com
LMK107BJ474MA	0.47 μ F	10 V		0603	
LMK212BJ105MG	1 μ F	10 V		0805	

Power Efficiency

The power conversion efficiency of the TPS60230 can be calculated by adding up the products of each LED current and voltage and dividing it by the product of the input voltage and current. With a fully charged battery where the input voltage is typically above the LED forward voltage, the charge pump operates in the 1x mode and efficiency is very high. As the battery discharges, there is a point where the current sources no longer have enough voltage overhead to maintain a constant current regulation. At that point, the charge pump switches into the 1.5x mode. The conversion efficiency is lowest at the crossover. As the battery discharges further, the efficiency again increases until at about 3.1 V where it reaches a second maximum. Below 3.1 V input voltage, the maximum current per LED is less than 25 mA.

Power Dissipation

The maximum power dissipation inside the TPS60230 can be calculated based on the following equation:

$$P_{D \max} = [(1.5 \times V_I) - V_O + 0.4 \text{ V}] \times I_O$$

The maximum power dissipation occurs when the input voltage is just low enough to operate in 1.5x mode, with a forward voltage of the white LED at maximum. This is typically for $V_I = 4.2 \text{ V}$ and a forward voltage of 3.6 V.

This needs to be lower than the maximum allowed power dissipation of the package, which can be calculated using the following equation:

$$P_{D \text{ max, package}} = \frac{T_{J\text{max}} - T_A}{T_{\theta ja}}$$

For example, the worst case power dissipation occurs at the input voltage level where the charge pump switches from the 1x mode to the 1.5x mode. At this operating point, the supply voltage to the current sources is at its maximum and the current sources must drop the most voltage in order to maintain a regulated output current. The worst case power dissipation occurs when all 5 LED outputs are fully loaded with 25 mA of LED current.

- With: $V_I = 4.2 \text{ V}$, $V_f = 3.6 \text{ V}$, $I_O = 125 \text{ mA}$ (1.5x mode)
- $P_{D \text{ max}} = 0.39 \text{ W}$

APPLICATION INFORMATION

TYPICAL APPLICATION OF A SMART PHONE DISPLAY WITH RESISTORS CONNECTED IN PARALLEL

If more than 25 mA of output current is needed, then the input pins to the current sinks can be connected in parallel as shown in the following application figure. This method can also be used to connect a LC display with only two connections for the white LEDs.

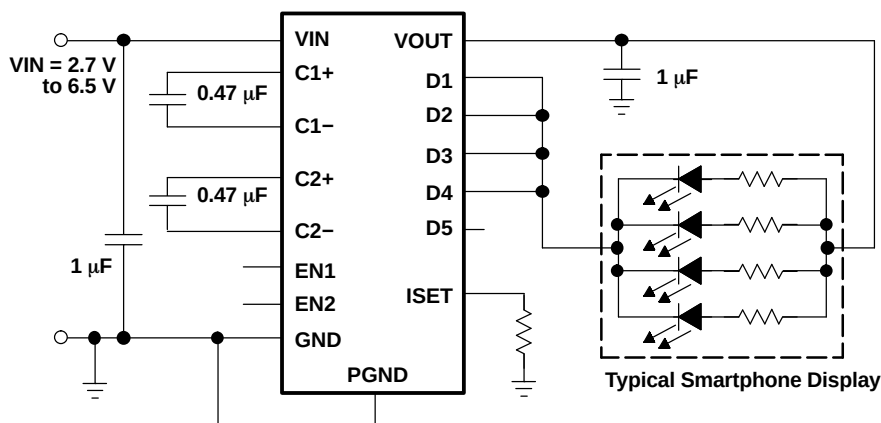


Figure 12. Typical Application With Resistors in Parallel

ANALOG DIMMING USING ISET PIN

The ISET pin can be used to connect an analog dc signal in the range of 0 mV to 600 mV (EN1 = EN2 = 1) for analog dimming of the white LEDs. For an input voltage of 0 V at ISET, the current is at its maximum, whereas at 600 mV, the LED current is zero. The maximum current is:

- For EN2 = EN1 = 1: $I_{LED} = V_{set}/R_{set} \times K = 0.6V/6k\Omega \times 270 = 27 \text{ mA per LED}$
- For EN2 = 1, EN0 = 1: $I_{LED} = V_{set}/R_{set} \times K = 0.4V/6k\Omega \times 270 = 18 \text{ mA per LED}$
- For EN2 = 0, EN1 = 1: $I_{LED} = V_{set}/R_{set} \times K = 0.2V/6k\Omega \times 270 = 9 \text{ mA per LED}$
- With EN2, EN1 set to 10 or 01, a voltage of 400 mV or 200 mV is required to set the LED current to zero.

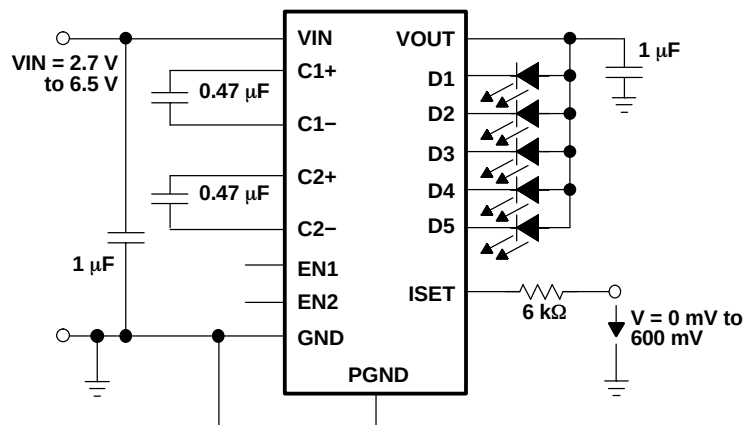


Figure 13. Analog Dimming Connections Using ISET Pin

TYPICAL APPLICATION USING 2 WHITE LEDs AND 6 GREEN LEDs FOR LCD BACKLIGHT AND

APPLICATION INFORMATION (continued)

KEYBOARD LIGHTING

The TPS60230 can be used to power any kind of LED. It is also possible to mix white LEDs with color LEDs which have a lower forward voltage. The LED with the highest forward voltage (typically the white LED) has to be connected to D1, because the output voltage of the charge pump is regulated in such a way to keep the voltage drop from D1 to GND at 400mV (with EN1 = EN2 = 1). Therefore the output voltage of the charge pump is regulated to:

$$V_{OUT} = V_{D1} + V_{FLEDD1}$$

V_{OUT} — Output voltage at VOUT

V_{D1} — Voltage from D1 to GND (V_{source} at D1 pin, see electrical characteristics)

V_{FLEDD1} — Forward voltage of the LED connected to D1

Resistor R_g is used to provide current sharing between the 6 green LEDs. The upper value is calculated using:

$$R_g = \frac{V_{FLEDD1} - V_{Fg}}{I_g}$$

V_{Fg} — Forward voltage of a green LED

I_g — Current per green LED

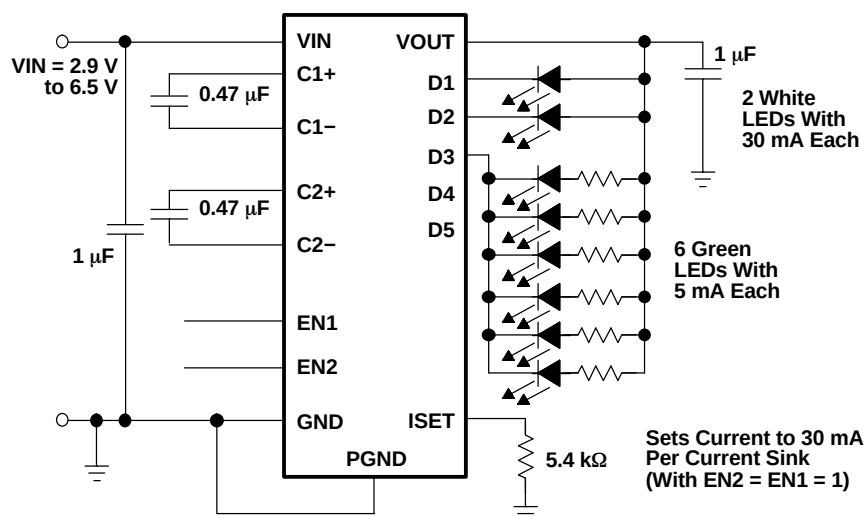


Figure 14. LED Connections for LCD Backlight and Keyboard Lighting

PROPOSED LAND PATTERN FOR PCB PRODUCTION

Refer to the application note SLUA271 for the proposed land pattern of the QFN package.

PHOTO FLASH APPLICATIONS

The TPS60230 can be used to power one or more white LEDs for photo flash applications. These applications usually require a certain current for a short period of time. The photo flash is typically turned on for 100 ms to 500 ms and turned off for a longer period of time. For such applications, when the TPS60230 is not turned on continuously, the device can support an output current of 150 mA. See Figure 5 for the input voltage needed for a certain LED forward voltage and LED current.

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TPS60230RGTR	Active	Production	VQFN (RGT) 16	3000 LARGE T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ
TPS60230RGTR.B	Active	Production	VQFN (RGT) 16	3000 LARGE T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ
TPS60230RGTRG4	Active	Production	VQFN (RGT) 16	3000 LARGE T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ
TPS60230RGTRG4.B	Active	Production	VQFN (RGT) 16	3000 LARGE T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ
TPS60230RGTT	Active	Production	VQFN (RGT) 16	250 SMALL T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ
TPS60230RGTT.B	Active	Production	VQFN (RGT) 16	250 SMALL T&R	Yes	NIPDAU	Level-2-260C-1 YEAR	-40 to 85	BIZ

⁽¹⁾ **Status:** For more details on status, see our [product life cycle](#).

⁽²⁾ **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

⁽⁴⁾ **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

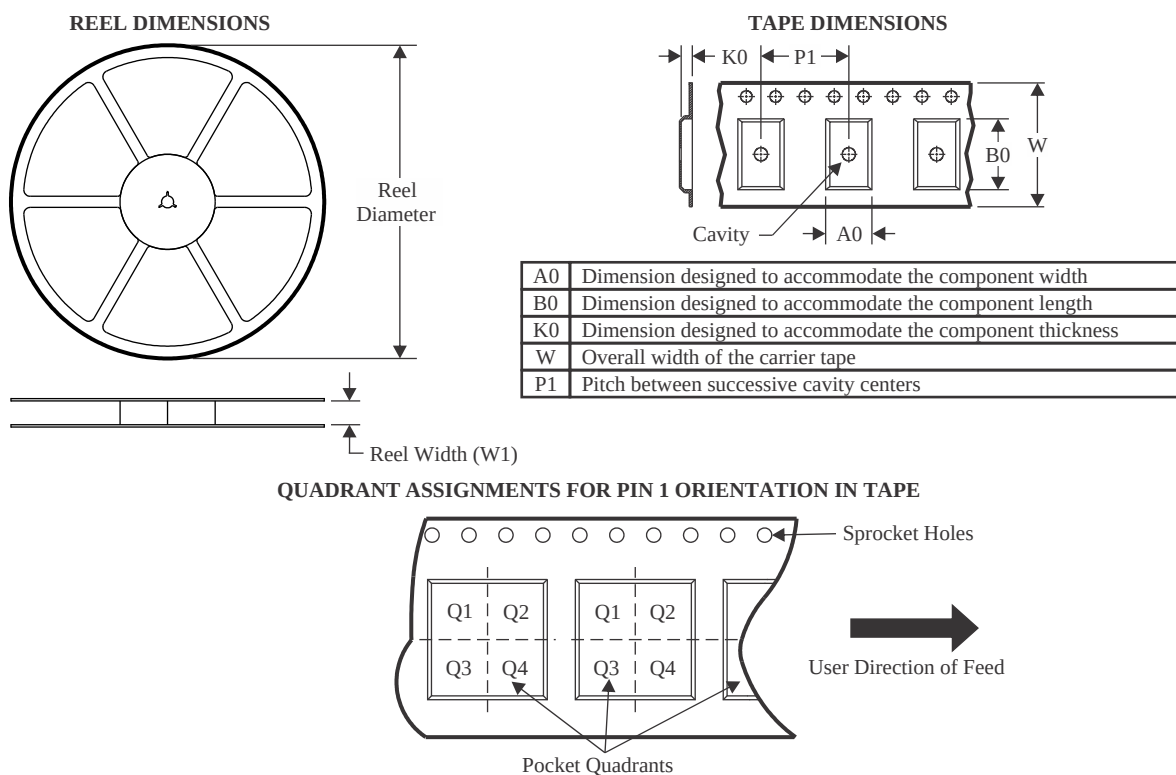
⁽⁶⁾ **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

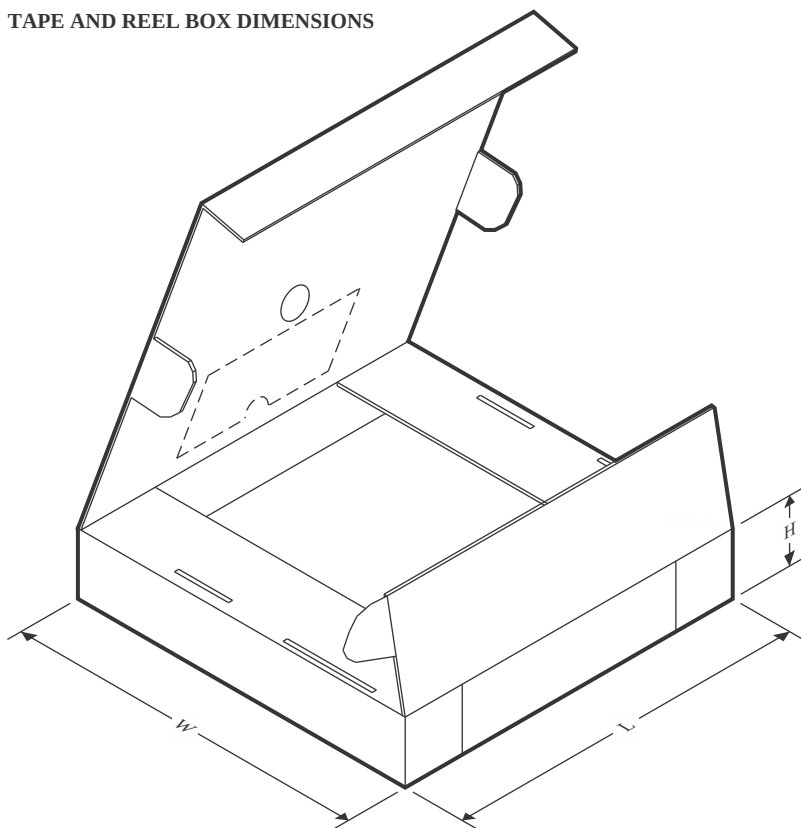
TAPE AND REEL INFORMATION



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS60230RGTR	VQFN	RGT	16	3000	330.0	12.4	3.3	3.3	1.1	8.0	12.0	Q2
TPS60230RGTRG4	VQFN	RGT	16	3000	330.0	12.4	3.3	3.3	1.1	8.0	12.0	Q2
TPS60230RGTT	VQFN	RGT	16	250	180.0	12.4	3.3	3.3	1.1	8.0	12.0	Q2

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

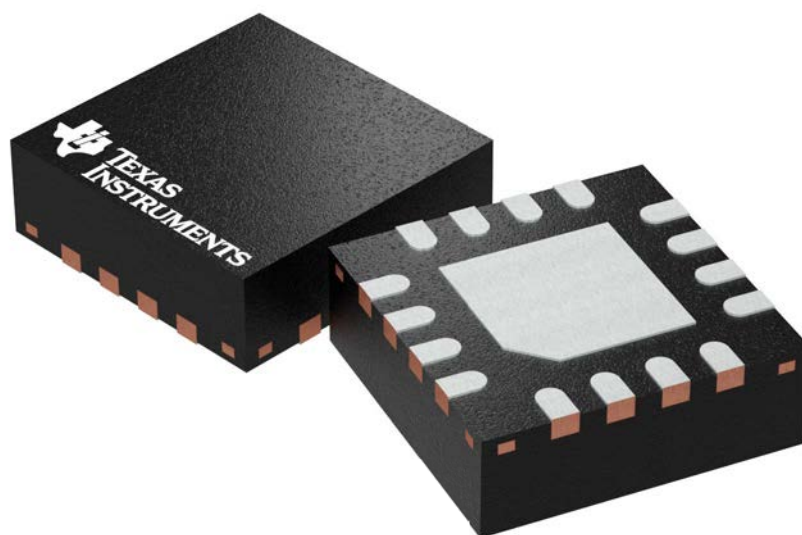
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS60230RGTR	VQFN	RGT	16	3000	356.0	356.0	35.0
TPS60230RGTRG4	VQFN	RGT	16	3000	356.0	356.0	35.0
TPS60230RGTT	VQFN	RGT	16	250	210.0	185.0	35.0

RGT 16

GENERIC PACKAGE VIEW

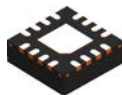
VQFN - 1 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

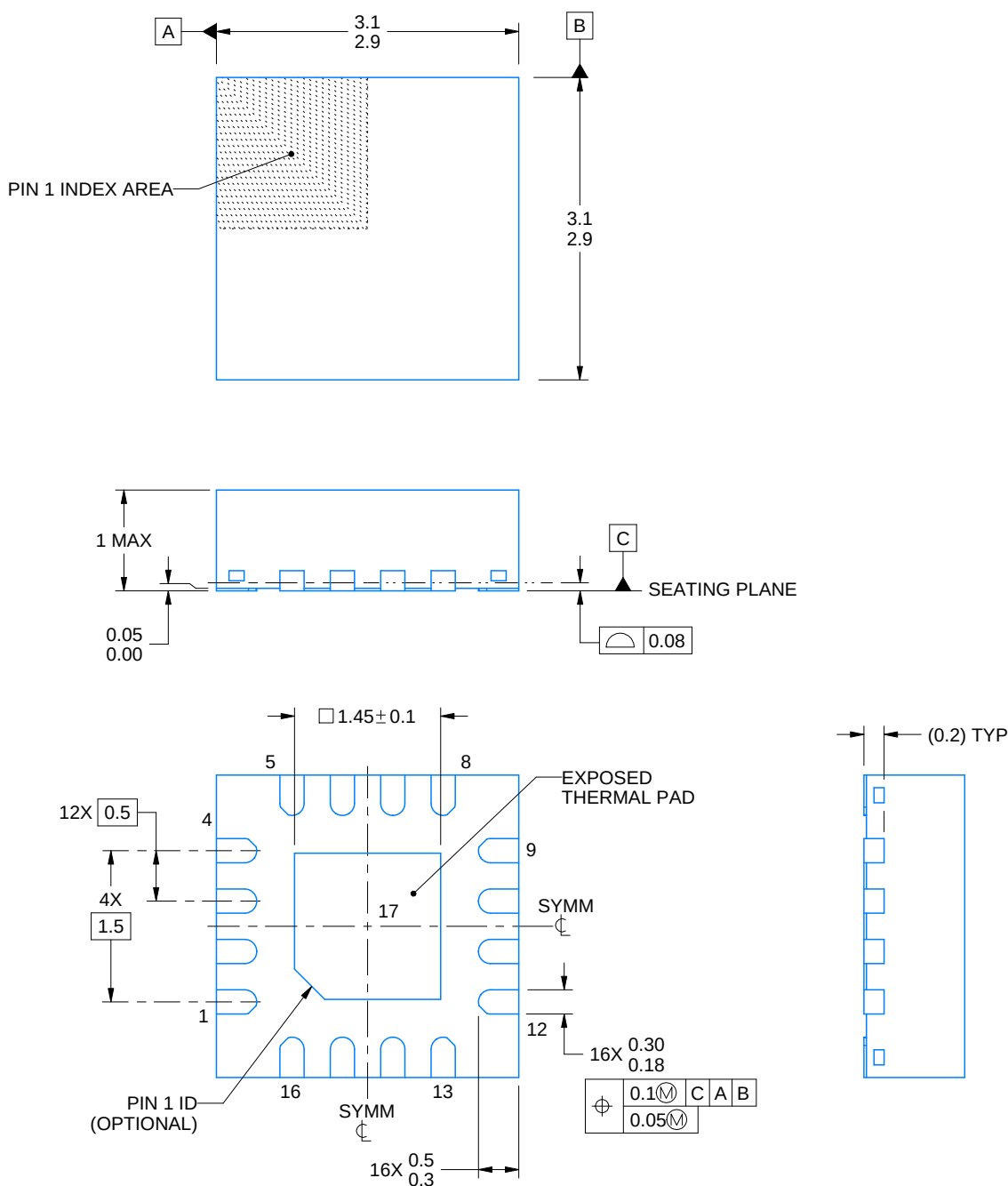
4203495/1



PACKAGE OUTLINE

VQFN - 1 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



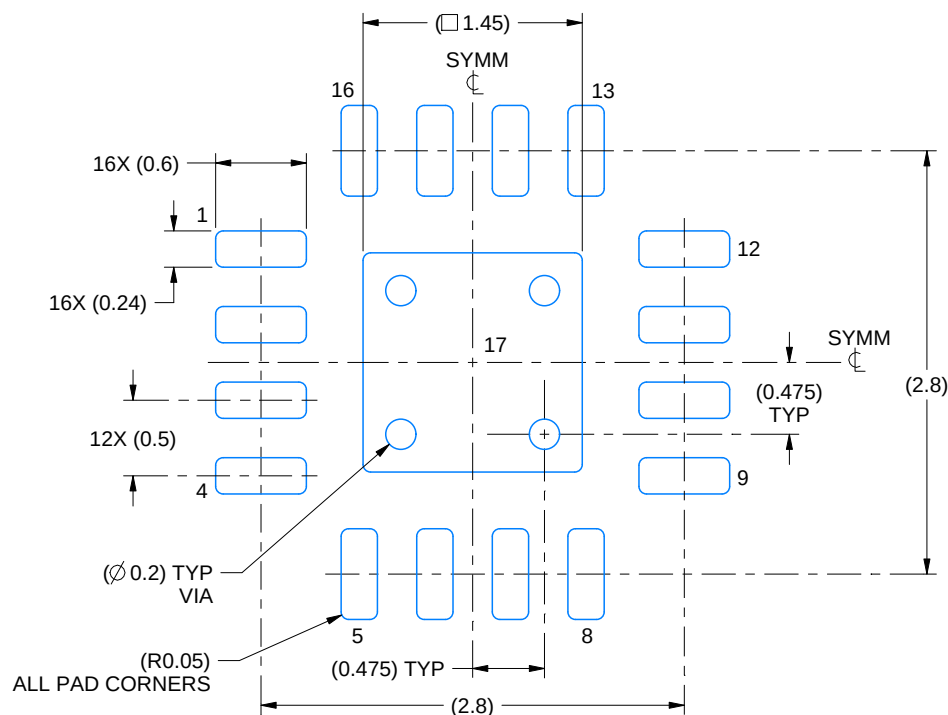
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
4. Reference JEDEC registration MO-220

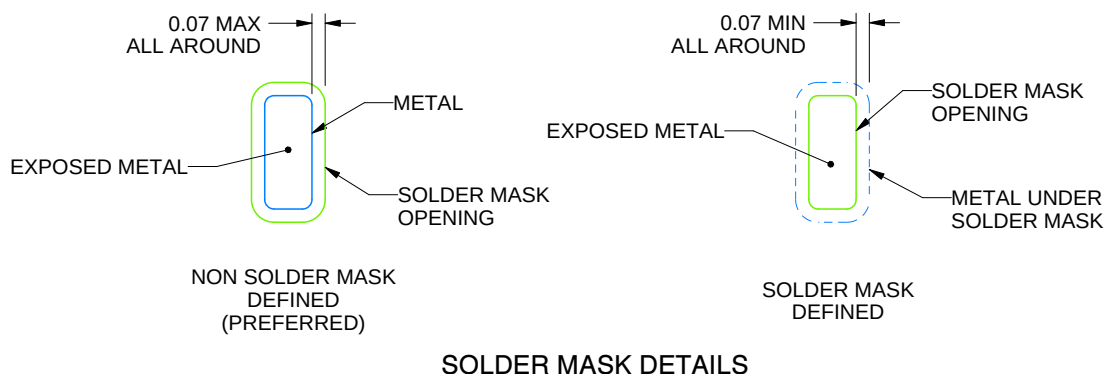
RGT0016A

VQFN - 1 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE:20X



4219032/A 02/2017

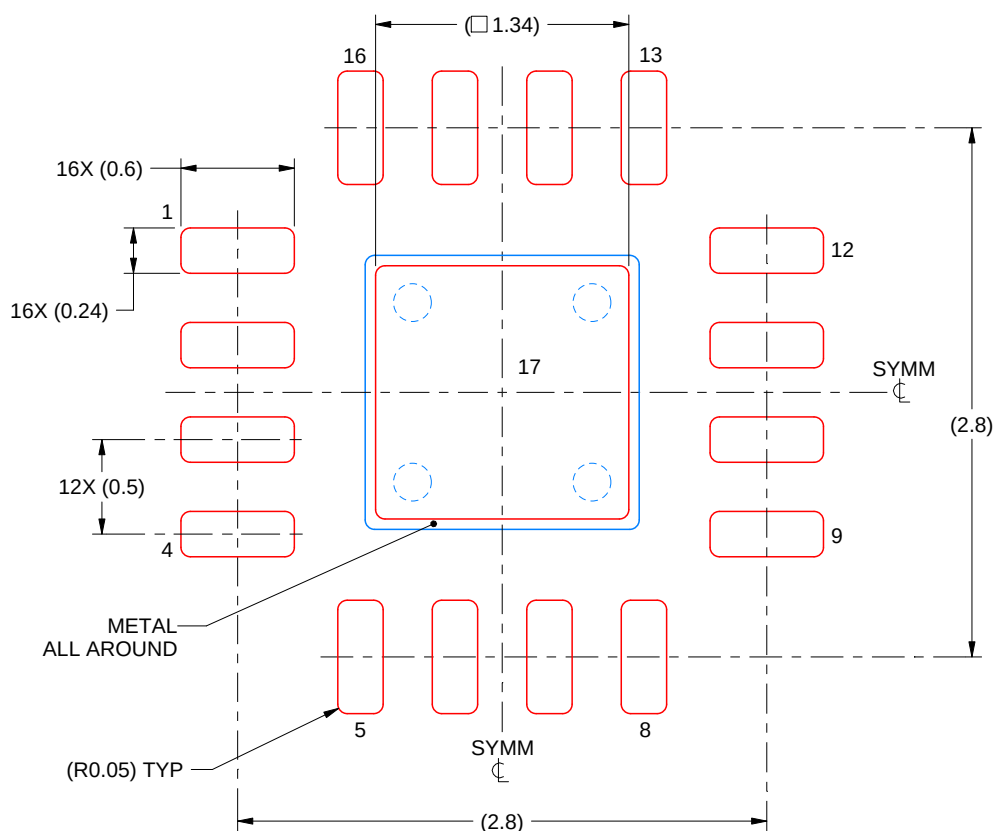
NOTES: (continued)

5. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).
6. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.

RGT0016A

VQFN - 1 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



SOLDER PASTE EXAMPLE BASED ON 0.125 mm THICK STENCIL

EXPOSED PAD 17:
86% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE
SCALE:25X

4219032/A 02/2017

NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2025, Texas Instruments Incorporated