

MOSFET - UniFET™, N-Channel

300 V, 28 A, 129 mΩ

FDB28N30TM

Description

UniFET MOSFET is onsemi's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

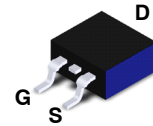
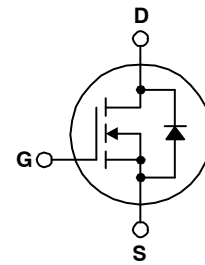
Features

- Typical $R_{DS(on)} = 108 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 14 \text{ A}$
- Low Gate Charge (Typical $Q_g = 39 \text{ nC}$)
- Low Reverse Transfer Capacitance C_{rss} (Typical $C_{rss} = 35 \text{ pF}$)
- 100% Avalanche Tested
- This Device is Pb-Free and is RoHS Compliant

Applications

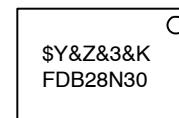
- Uninterruptible Power Supply
- AC-DC Power Supply

N-Channel



D²PAK-3 (TO-263, 3-LEAD)
CASE 418AJ

MARKING DIAGRAM



\$Y	= onsemi Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FDB28N30	= Specific Device Code

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MOSFET MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	300	V
V_{GSS}	Gate to Source Voltage	± 30	V
I_D	Drain Current –Continuous ($T_C = 25^\circ\text{C}$)	28	A
	–Continuous ($T_C = 100^\circ\text{C}$)	19	
I_{DM}	Drain Current –Pulsed (Note 1)	112	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	588	mJ
I_{AR}	Avalanche Current (Note 1)	28	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	25	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	250	W
	–Derate above 25°C	2.0	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to $+150$	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds	300	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature
2. $L = 1.5\text{ mH}$, $I_{AS} = 28\text{ A}$, $V_{DD} = 50\text{ V}$, $R_G = 25\ \Omega$, starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 28\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^\circ\text{C}$

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Units
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	0.5	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Maximum Thermal Resistance, Junction to Ambient (1 in2 Pad of 2-oz Copper)	40	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Maximum Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper)	62.5	

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ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250\ \mu\text{A}$, $V_{GS} = 0\ \text{V}$, $T_J = 25^\circ\text{C}$	300			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$, referenced to 25°C		0.4		V/ $^\circ\text{C}$
I_{DSS}	Drain-to-Source Leakage Current	$V_{DS} = 300\ \text{V}$, $V_{GS} = 0\ \text{V}$			1	μA
		$V_{DS} = 240\ \text{V}$, $T_C = 125^\circ\text{C}$			10	
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30\ \text{V}$, $V_{DS} = 0\ \text{V}$			± 100	nA

ON CHARACTERISTICS

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\ \mu\text{A}$	3.0		5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\ \text{V}$, $I_D = 14\ \text{A}$		0.108	0.129	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 40\ \text{V}$, $I_D = 14\ \text{A}$		24.8		s

DYNAMIC CHARACTERISTICS

C_{iss}	Input Capacitance	$V_{DS} = 25\ \text{V}$, $V_{GS} = 0\ \text{V}$, $f = 1\ \text{MHz}$		1690	2250	pF
C_{oss}	Output Capacitance			305	405	pF
C_{rss}	Reverse Transfer Capacitance			35	50	pF
Q_g	Total Gate Charge at 10 V	$V_{DS} = 240\ \text{V}$, $I_D = 28\ \text{A}$,				

TYPICAL PERFORMANCE CHARACTERISTICS

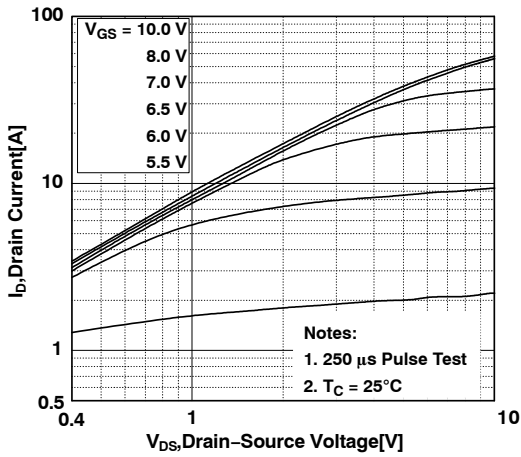


Figure 1. On-Region Characteristics

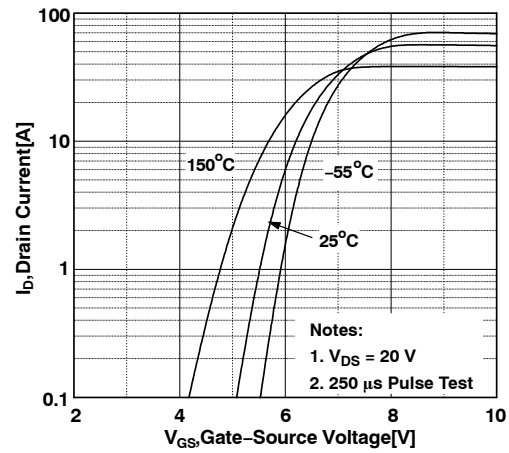


Figure 2. Transfer Characteristics

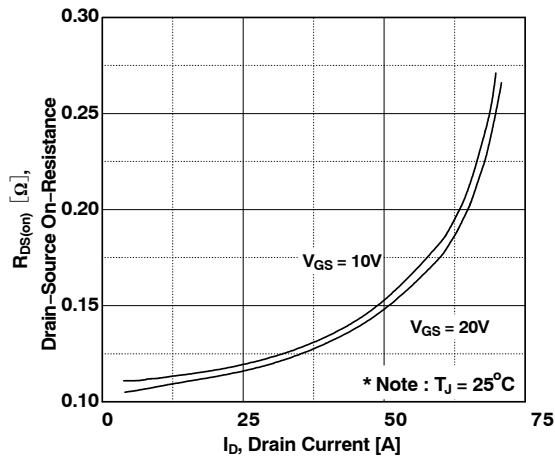


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

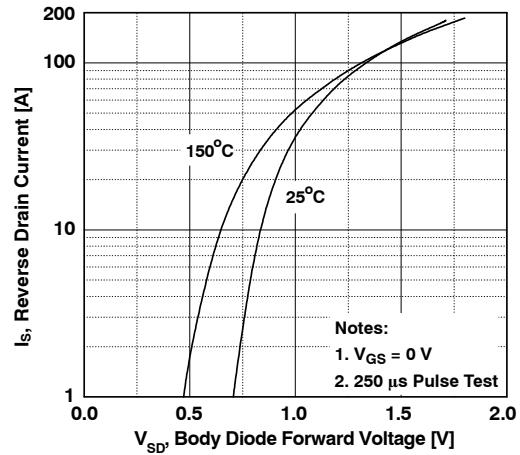


Figure 4. Body Diode Forward Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

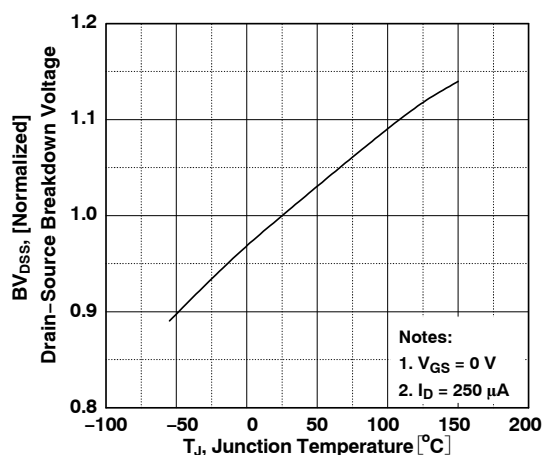


Figure 7. Breakdown Voltage Variation vs. Temperature

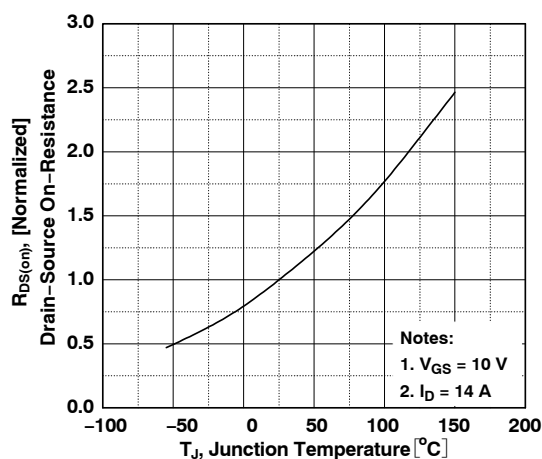


Figure 8. On-Resistance Variation vs. Temperature

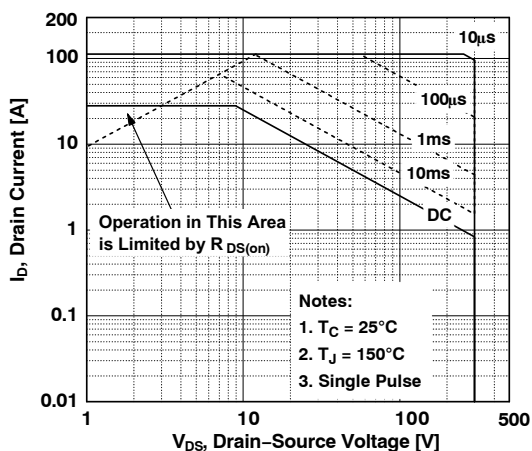


Figure 9. Maximum Safe Operating Area

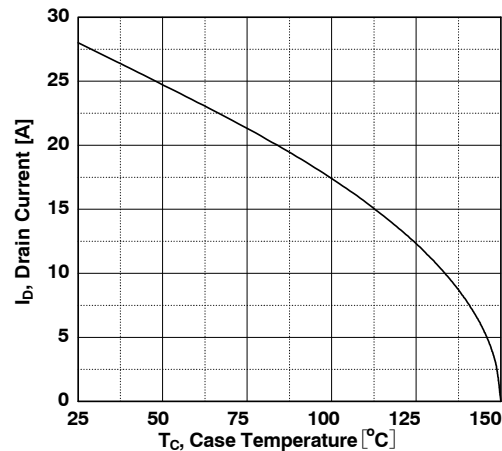


Figure 10. Maximum Drain Current vs. Case Temperature

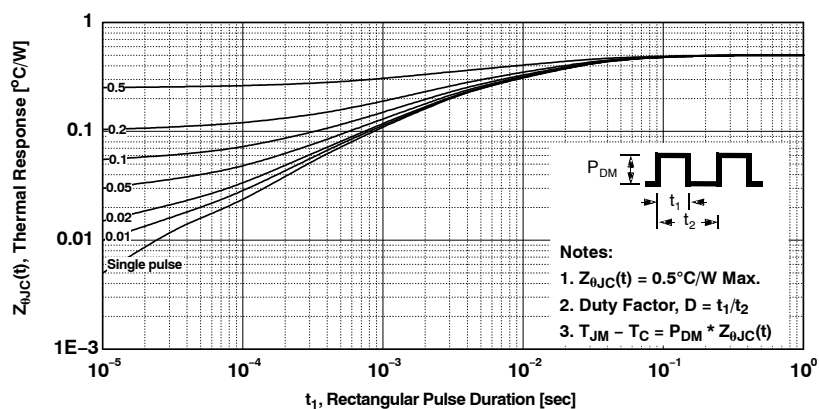


Figure 11. Transient Thermal Response Curve

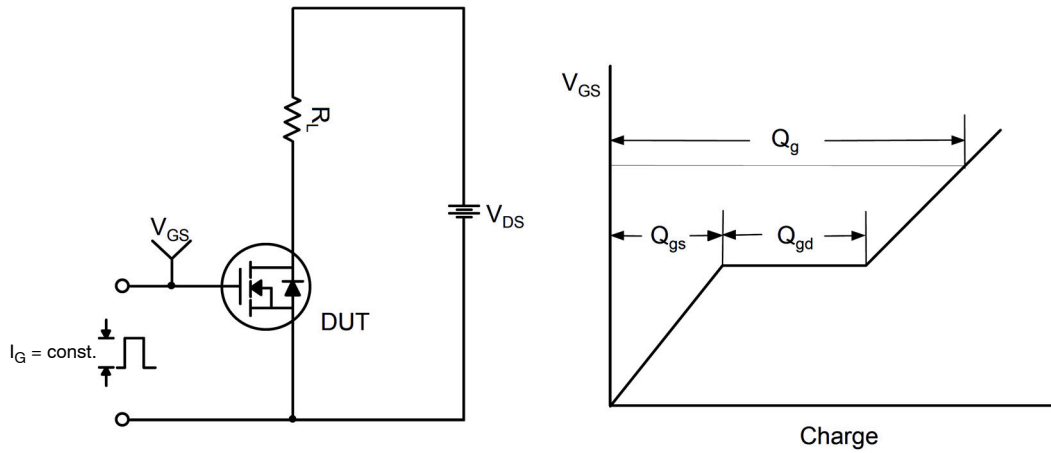


Figure 12. Gate Charge Test Circuit & Waveform

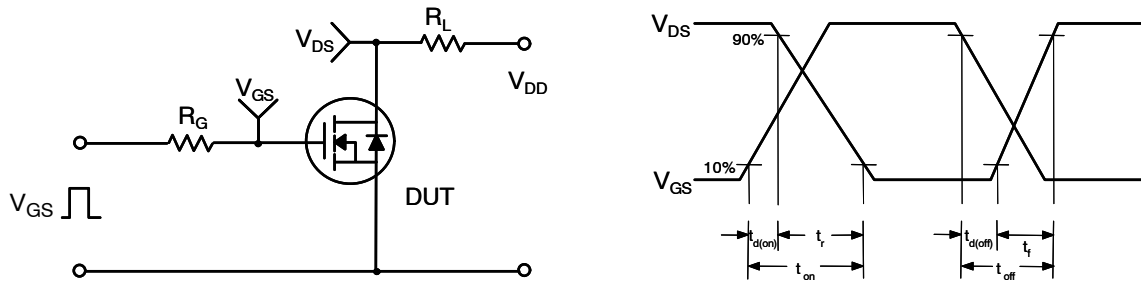


Figure 13. Resistive Switching Test Circuit & Waveform

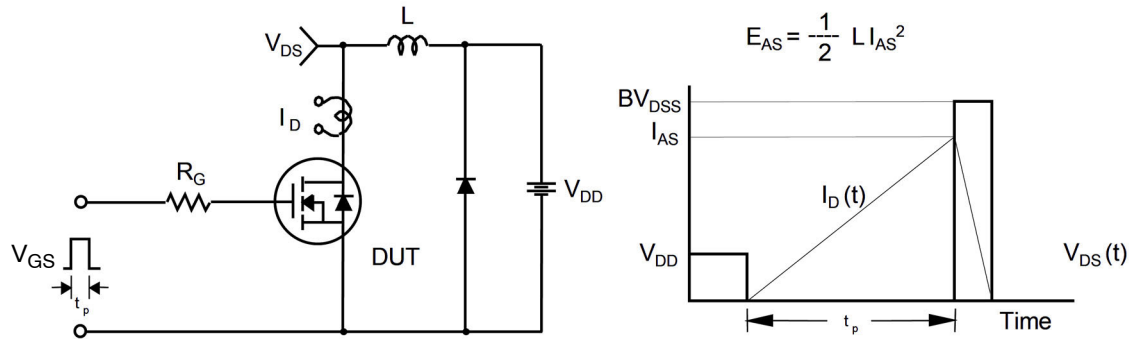


Figure 14. Unclamped inductive Switching Test Circuit & Waveform

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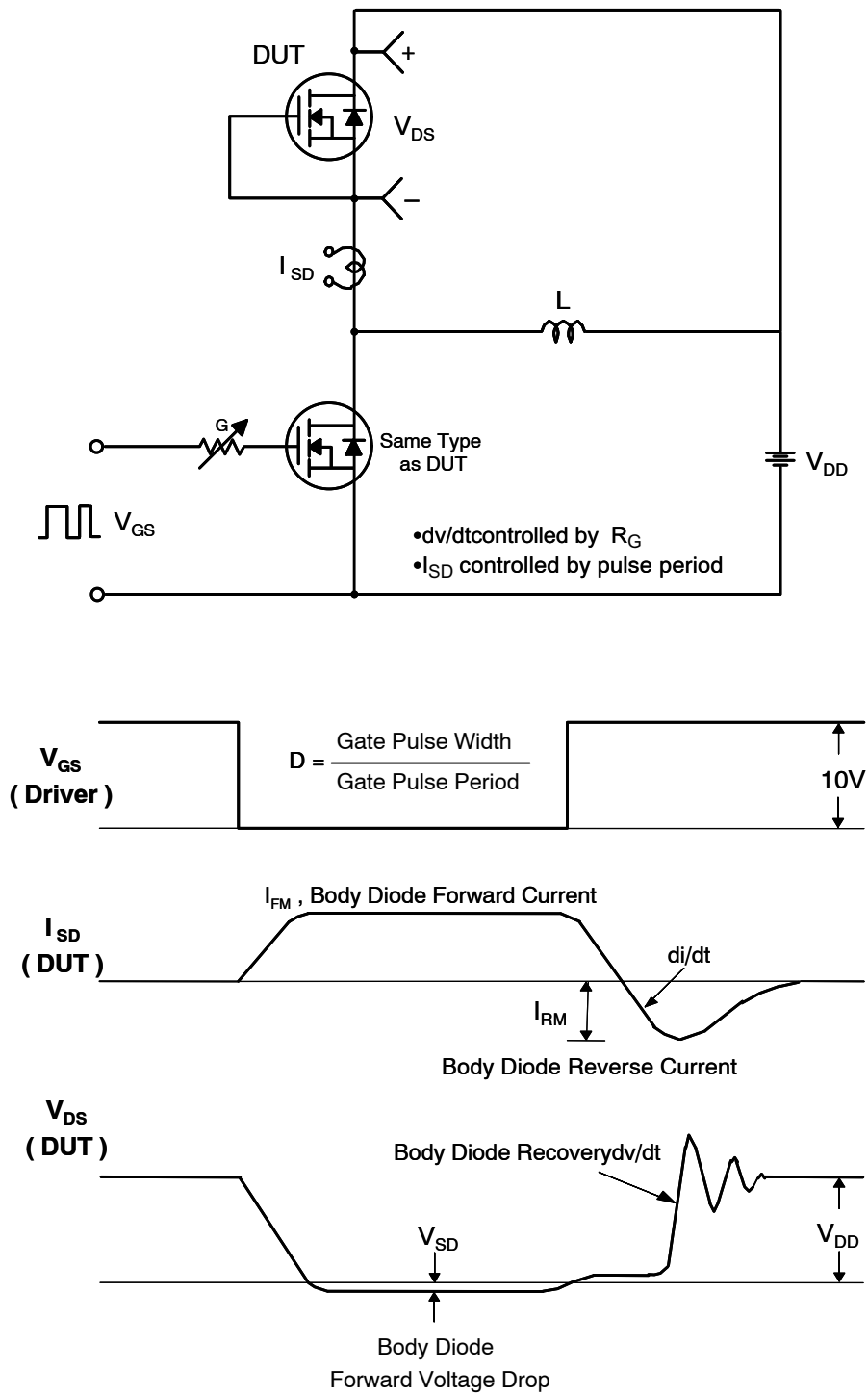
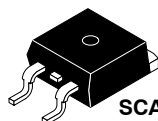


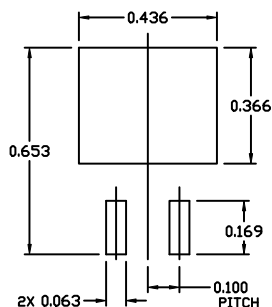
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveform



SCALE 1:1

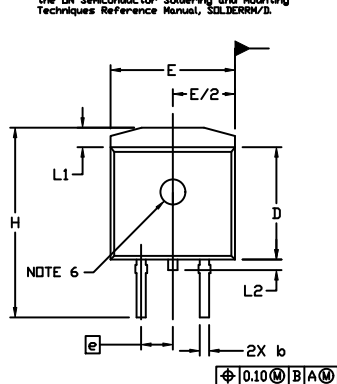
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ISSUE F

DATE 11 MAR 2021



RECOMMENDED
MOUNTING FOOTPRINT

For additional information on our Pb-free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: INCHES
3. CHAMFER OPTIONAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1, AND E1.
6. OPTIONAL MOLD FEATURE.
7. ①, ② ... OPTIONAL CONSTRUCTION FEATURE CALL OUTS.

DIM	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	0.160			

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